

Refine Search

Search Results -

Term	Documents
RENAM\$3	0
RENAM	1
RENAME	3005
RENAMED	3789
RENAMEL	7
RENAMER	87
RENAMERS	15
RENAMES	808
RENAME0	1
RENAME1	5
RENAME2	4
(L25 AND (RENAM\$3 OR MAP\$5 OR REMAP\$5)).PGPB,USPT.	70

There are more results than shown above. [Click here to view the entire set.](#)

Database:

US Pre-Grant Publication Full-Text Database
 US Patents Full-Text Database
 US OCR Full-Text Database
 EPO Abstracts Database
 JPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Search:

L26

Refine Search

Recall Text

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Search History

DATE: Tuesday, June 15, 2004 [Printable Copy](#) [Create Case](#)

Set
Name Query

side by
 side

DB=PGPB,USPT; PLUR=YES; OP=OR

Hit
Count

Set
Name
 result
 set

<u>L26</u>	L25 and (renam\$3 or map\$5 or remap\$5)	70	<u>L26</u>
<u>L25</u>	L24 and l15	106	<u>L25</u>
<u>L24</u>	l20 or l21 or l22 or L23	16565	<u>L24</u>
<u>L23</u>	(718/100-108)![CCLS]	3660	<u>L23</u>
<u>L22</u>	(719/311-332)[CCLS]	3730	<u>L22</u>
<u>L21</u>	(712/2-300)[CCLS]	9891	<u>L21</u>
<u>L20</u>	(712/2-300)![CCLS]	9891	<u>L20</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L19</u>	threads near8 (differnt or convert\$4 or conversion\$1 or switch\$3 or exchang\$5) near5 register\$1 and (common or shar\$6) near6 cache	25	<u>L19</u>
<u>L18</u>	threads near8 (differnt or convert\$4 or conversion\$1 or switch\$3 or exchang\$5) near5 register\$1 and (common or shar\$6) near6 cache	25	<u>L18</u>
<u>L17</u>	threads near8 (differnt or convert\$4 or conversion\$1 or switch\$3 or exchang\$5) near5 register\$1 and (common or shar\$6)	106	<u>L17</u>
<u>L16</u>	threads near8 (differnt or convert\$4 or conversion\$1 or switch\$3 or exchang\$5) near5 register\$1	139	<u>L16</u>
<u>L15</u>	threads near8 (differnt or convert\$4 or conversion\$1 or switch\$3 or exchang\$5) near7 register\$1	170	<u>L15</u>
<u>L14</u>	threads near8 renam\$3 near5 register\$1	18	<u>L14</u>
<u>L13</u>	thread\$3 and cross\$3 near4 decod\$3 near6 register\$1	3	<u>L13</u>
<u>L12</u>	thread\$3 near12 tranparent near8 register\$1	0	<u>L12</u>
<u>L11</u>	thread\$3 near9 (redefin\$6 or reconfigur\$5) near6 register\$1	5	<u>L11</u>
<u>L10</u>	L8 not l9	3	<u>L10</u>
<u>L9</u>	L8 not l5	20	<u>L9</u>
<u>L8</u>	L7 near15 (shar\$6 or common)	23	<u>L8</u>
<u>L7</u>	thread\$3 near9 (chang\$5 or switch\$5 or renam\$3 or exchang\$5) near6 (name\$1 or register\$1 or operand\$1)	303	<u>L7</u>
<u>L6</u>	thread\$3 near9 revers\$6 near5 map\$5	2	<u>L6</u>
<u>L5</u>	(first or second or plur\$7 or multiple or two or dual or primary or each) near7 thread\$1 near12 (respective\$4 or correspond\$3) near9 register\$1	34	<u>L5</u>
<u>L4</u>	(first or second or plur\$7 or multiple or two or dual or primary or each) near7 thread\$1 near12 decod\$3 near9 register\$1	10	<u>L4</u>
<u>L3</u>	L2 not l1	1	<u>L3</u>
<u>L2</u>	(first or second or plur\$7 or multiple or two or dual or primary or each) near7 thread\$1 near12 map\$6 near9 register\$1	9	<u>L2</u>
<u>L1</u>	(first or second or plur\$7 or multiple or two or dual or primary or each) near5 thread\$1 near8 map\$6 near6 register\$1	8	<u>L1</u>

END OF SEARCH HISTORY

Refine Search

Your wildcard search against 10000 terms has yielded the results below.

Your result set for the last L# is incomplete.

The probable cause is use of unlimited truncation. Revise your search strategy to use limited truncation.

Search Results -

Term	Documents
SECOND	5698519
SECONDS	524069
MULTIPLE	1460021
MULTIPLES	50024
NEXT	1916025
NEXTS	51
DUAL	416747
DUALS	382
TWO	7638574
TWOES	46
TWOS	3254
((SECOND OR PLUR\$7 OR MULTIPLE OR NEXT OR DUAL OR TWO OR PARENT OR CHILD) NEAR4 THREAD\$3 NEAR25 FIFO\$1).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	42

There are more results than shown above. [Click here to view the entire set.](#)

Database:

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US Patents Full-Text Database
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Search:

L15 not l14

Refine Search

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Search History

DATE: Tuesday, June 15, 2004 [Printable Copy](#) [Create Case](#)

<u>Set</u> <u>Name</u>	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L15</u>	(second or plur\$7 or multiple or next or dual or two or parent or child) near4 thread\$3 near25 fifo\$1	42	<u>L15</u>
<u>L14</u>	(second or plur\$7 or multiple or next or dual or two or parent or child) near4 thread\$3 near25 read\$3 near25 (writ\$5 or written) near15 (buffer\$1 or register\$1)	29	<u>L14</u>
<u>L13</u>	(second or plur\$7 or multiple or next or dual or two or parent or child) near4 thread\$3 near15 read\$3 near15 (writ\$5 or written)	218	<u>L13</u>
<u>L12</u>	L11 near25 compar\$7	4	<u>L12</u>
<u>L11</u>	(second or plur\$7 or multiple or next or dual or two or parent or child) near4 thread\$3 near25 read\$3 near25 (writ\$5 or written)	236	<u>L11</u>
<u>L10</u>	thread\$3 near25 read\$3 near25 (writ\$5 or written)	1239	<u>L10</u>
<u>L9</u>	thread\$1 near25 compar\$7 near25 read\$3 near15 (writ\$5 or written)	25	<u>L9</u>
<u>L8</u>	thread\$1 near25 compar\$7 near25 (writ\$5 or written or read\$3)	368	<u>L8</u>
<u>L7</u>	thead\$1 near25 compar\$7 near25 (writ\$5 or written or read\$3)	0	<u>L7</u>
<u>L6</u>	thead\$1 near25 compar\$7 near25 (writ\$5 or written or read\$3)	0	<u>L6</u>
<u>L5</u>	thead\$1 near15 compar\$7 near15 read\$3 near15 (writ\$5 or written)	0	<u>L5</u>
<u>L4</u>	L2 near8 read\$3 near9 (writ\$5 or written)	0	<u>L4</u>
<u>L3</u>	L2 near8 receiv\$3	7	<u>L3</u>
<u>L2</u>	thread\$1 near8 compar\$7 near9 (tag\$1 or context\$1 or flag\$1 or id or identification\$1 or identif\$6)	162	<u>L2</u>
<u>L1</u>	(map\$5 or associat\$4) near7 register\$1 near8 (id\$1 or identification\$1 or identif\$6 or name\$1 or variable\$1 or operand\$1) near8 thread\$1	37	<u>L1</u>

END OF SEARCH HISTORY

Hit List

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Search Results - Record(s) 21 through 40 of 48 returned.

☐ 21. Document ID: US 6664960 B2

Using default format because multiple data bases are involved.

L25: Entry 21 of 48

File: USPT

Dec 16, 2003

US-PAT-NO: 6664960

DOCUMENT-IDENTIFIER: US 6664960 B2

TITLE: Apparatus for processing non-planar video graphics primitives and associated method of operation

DATE-ISSUED: December 16, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Goel; Vineet	Winter Park	FL		
Hartog; Robert S.	Windermere	FL		
Mang; Michael A.	Oviedo	FL		

US-CL-CURRENT: 345/423

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw D
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☐ 22. Document ID: US 6658447 B2

L25: Entry 22 of 48

File: USPT

Dec 2, 2003

US-PAT-NO: 6658447

DOCUMENT-IDENTIFIER: US 6658447 B2

**** See image for Certificate of Correction ****

TITLE: Priority based simultaneous multi-threading

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw D
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☐ 23. Document ID: US 6647508 B2

L25: Entry 23 of 48

File: USPT

Nov 11, 2003

US-PAT-NO: 6647508

DOCUMENT-IDENTIFIER: US 6647508 B2

TITLE: Multiprocessor computer architecture with multiple operating system instances and software controlled resource allocation

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMNC	Draw De
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☐ 24. Document ID: US 6633916 B2

L25: Entry 24 of 48

File: USPT

Oct 14, 2003

US-PAT-NO: 6633916

DOCUMENT-IDENTIFIER: US 6633916 B2

TITLE: Method and apparatus for virtual resource handling in a multi-processor computer system

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMNC	Draw De
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☐ 25. Document ID: US 6567839 B1

L25: Entry 25 of 48

File: USPT

May 20, 2003

US-PAT-NO: 6567839

DOCUMENT-IDENTIFIER: US 6567839 B1

TITLE: Thread switch control in a multithreaded processor system

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMNC	Draw De
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☐ 26. Document ID: US 6542926 B2

L25: Entry 26 of 48

File: USPT

Apr 1, 2003

US-PAT-NO: 6542926

DOCUMENT-IDENTIFIER: US 6542926 B2

TITLE: Software partitioned multi-processor system with flexible resource sharing levels

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMNC	Draw De
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☐ 27. Document ID: US 6542921 B1

L25: Entry 27 of 48

File: USPT

Apr 1, 2003

US-PAT-NO: 6542921

DOCUMENT-IDENTIFIER: US 6542921 B1

TITLE: Method and apparatus for controlling the processing priority between multiple threads in a multithreaded processor

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw Da
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☐ 28. Document ID: US 6496925 B1

L25: Entry 28 of 48

File: USPT

Dec 17, 2002

US-PAT-NO: 6496925

DOCUMENT-IDENTIFIER: US 6496925 B1

TITLE: Method and apparatus for processing an event occurrence within a
multithreaded processor

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw Da
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☒ 29. Document ID: US 6496909 B1

L25: Entry 29 of 48

File: USPT

Dec 17, 2002

US-PAT-NO: 6496909

DOCUMENT-IDENTIFIER: US 6496909 B1

TITLE: Method for managing concurrent access to virtual memory data structures

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw Da
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☐ 30. Document ID: US 6401155 B1

L25: Entry 30 of 48

File: USPT

Jun 4, 2002

US-PAT-NO: 6401155

DOCUMENT-IDENTIFIER: US 6401155 B1

TITLE: Interrupt/software-controlled thread processing

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw Da
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☐ 31. Document ID: US 6389449 B1

L25: Entry 31 of 48

File: USPT

May 14, 2002

US-PAT-NO: 6389449

DOCUMENT-IDENTIFIER: US 6389449 B1

TITLE: Interstream control and communications for multi-streaming digital
processors

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw Da
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☐ 32. Document ID: US 6381682 B2

L25: Entry 32 of 48

File: USPT

Apr 30, 2002

US-PAT-NO: 6381682

DOCUMENT-IDENTIFIER: US 6381682 B2

TITLE: Method and apparatus for dynamically sharing memory in a multiprocessor system

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 33. Document ID: US 6374286 B1

L25: Entry 33 of 48

File: USPT

Apr 16, 2002

US-PAT-NO: 6374286

DOCUMENT-IDENTIFIER: US 6374286 B1

TITLE: Real time processor capable of concurrently running multiple independent JAVA machines

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 34. Document ID: US 6357016 B1

L25: Entry 34 of 48

File: USPT

Mar 12, 2002

US-PAT-NO: 6357016

DOCUMENT-IDENTIFIER: US 6357016 B1

**** See image for Certificate of Correction ****

TITLE: Method and apparatus for disabling a clock signal within a multithreaded processor

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 35. Document ID: US 6332180 B1

L25: Entry 35 of 48

File: USPT

Dec 18, 2001

US-PAT-NO: 6332180

DOCUMENT-IDENTIFIER: US 6332180 B1

TITLE: Method and apparatus for communication in a multi-processor computer system

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 36. Document ID: US 6317872 B1

L25: Entry 36 of 48

File: USPT

Nov 13, 2001

US-PAT-NO: 6317872

DOCUMENT-IDENTIFIER: US 6317872 B1

TITLE: Real time processor optimized for executing JAVA programs

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw. De
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☐ 37. Document ID: US 6260068 B1

L25: Entry 37 of 48

File: USPT

Jul 10, 2001

US-PAT-NO: 6260068

DOCUMENT-IDENTIFIER: US 6260068 B1

TITLE: Method and apparatus for migrating resources in a multi-processor computer system

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw. De
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☐ 38. Document ID: US 6247109 B1

L25: Entry 38 of 48

File: USPT

Jun 12, 2001

US-PAT-NO: 6247109

DOCUMENT-IDENTIFIER: US 6247109 B1

TITLE: Dynamically assigning CPUs to different partitions each having an operation system instance in a shared memory space

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw. De
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☐ 39. Document ID: US 6226734 B1

L25: Entry 39 of 48

File: USPT

May 1, 2001

US-PAT-NO: 6226734

DOCUMENT-IDENTIFIER: US 6226734 B1

TITLE: Method and apparatus for processor migration from different processor states in a multi-processor computer system

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KIMC	Draw. De
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☐ 40. Document ID: US 6223208 B1

L25: Entry 40 of 48

File: USPT

Apr 24, 2001

US-PAT-NO: 6223208

DOCUMENT-IDENTIFIER: US 6223208 B1

TITLE: Moving data in and out of processor units using idle register/storage
functional units

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	RIMC	Drawings
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Term	Documents
PRIORIT\$7	0
PRIORIT	610
PRIORITA	1
PRIORITABLE	1
PRIORITAET	1
PRIORITARILY	3
PRIORITARY	18
PRIORITAS	2
PRIORITASATION	1
PRIORITAT	1
PRIORITATEN	1
(PRIORIT\$7 NEAR12 THREAD\$3 NEAR9 BIT\$1).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	48

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